



Our Reference: DFS-138-A

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#6/Kre A
9/16/02
Smith

Applicant: Martin Gehrke & Torsten Pechstein
Serial Number: 10/001,668
Filing Date: October 23, 2001
Examiner/Art Group Unit: Unknown/Unknown
Title: A DESIGN FOR CONSTRUCTING AN INPUT
CIRCUIT TO RECEIVE AND PROCESS AN
ELECTRICAL SIGNAL

PRELIMINARY AMENDMENT

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

If any charges or fees must be paid in connection with the following communication, they may be paid out of our Deposit Account No. 25-0115.

Prior to initial examination, please amend the above-identified patent application as indicated below.

In the claims:

1. (Amended) A design for constructing an input circuit to receive and process an electrical signal, such as a voltage signal from a voltage source, specifically from a sensor where the input circuit has an extremely high input resistance of at least 10^{11} ohms and is located on a printed circuit board, where a first area carrying input circuit components of the printed circuit board is separated by a channel-shaped recess from a surrounding second area, characterized in that the channel-shaped recess terminates in the interior of the printed circuit board and is extended in the direction of the thickness of the printed circuit board as far as a moisture-impervious barrier layer which underlies the first area of the printed circuit

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